

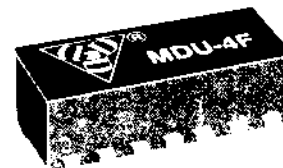
**Fast Logic**  
**Multiple Digital  
 Delay Units**

**SERIES: MDU-4F**  
 (14 pins DIP) TTL Interfaced



**Features:**

- Completely interfaced for TTL & DTL application.
- P.C. board space economy achieved.
- Fits standard 14 pins DIP socket.
- 4 equal delay units/package.



**Specifications:**

- Delay Tolerance:  $\pm 5\%$  or 2 ns whichever is greater.
- Rise-time: 2 ns typically.
- Temperature coefficient: 100 PPM/ C.
- Temperature range: 0° to + 70 C.
- Supply voltage: 5 Vdc  $\pm 5\%$ .
- Supply Current:  
 I<sub>cc1</sub>: 110 ma.  
 I<sub>cc2</sub>: 24 ma.
- DC parameters: See TTL-Fast Schottky Logic Table on Page 6.

**Test Conditions:**

- Input Pulse Width:  $\approx 150\%$  of total delay.
- Time delay measured @ 1.5 V on rising edge.
- Rise-time is measured from .75 V to 2.4 V of leading time
- All measurements made @ V<sub>cc</sub> = 5V;  
 T<sub>A</sub> = +25°C.

Part No.	Delay Per Line (ns)
MDU-4F-5	5
MDU-4F-10	10
MDU-4F-15	15
MDU-4F-20	20
MDU-4F-25	25
MDU-4F-30	30
MDU-4F-35	35
MDU-4F-40	40
MDU-4F-45	45
MDU-4F-50	50

Other delay times available on request.

