

# Gated-Delay Line Oscillator

2-Phase

16 pins DIP  
ECL Interfaced

SERIES: DLO-50

**data  
delay  
devices, inc.**



## Features:

- Continuous or keyable wavetrain.
- Locked synchronization achieved with random gating signal.
- Low profile module.
- Available from 50 MHZ to 100 MHZ frequencies.
- Complimentary outputs.



## Specifications:

- Frequencies: See table; others on request.
- Tolerance:  $\pm 2\%$ .
- Temperature coefficient: 100 PPM/°C.
- Operating temperature: 0°-70°C.
- Supply voltage: 5 Vdc  $\pm 5\%$ .
- Supply current: 90 ma. typ.
- Storage temperature: -55°C to 125°C.
- Inherent delay ( $T_{DO}$ ): 1 ns typ.
- Pulse skewing = .5 ns typ.
- DC parameters: See ECL-10KH Logic Table on Page 6.

| Part Number | Frequency |
|-------------|-----------|
| DLO-50-50   | 50 MHZ    |
| DLO-50-55   | 55 MHZ    |
| DLO-50-60   | 60 MHZ    |
| DLO-50-65   | 65 MHZ    |
| DLO-50-70   | 70 MHZ    |
| DLO-50-75   | 75 MHZ    |
| DLO-50-80   | 80 MHZ    |
| DLO-50-85   | 85 MHZ    |
| DLO-50-90   | 90 MHZ    |
| DLO-50-95   | 95 MHZ    |
| DLO-50-100  | 100 MHZ   |

