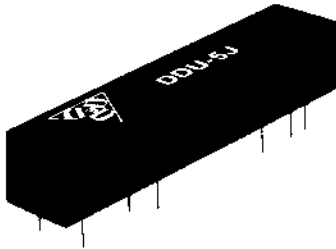


Digital Delay Units

SERIES: DDU-5J

**10 Taps (32 pins DIP)
T²L Interfaced**



Features:

- Completely interfaced for TTL and DTL applications.
- No external components required.
- 10 Taps equally spaced.
- P. C. board space economy achieved.

Specifications:

- **Delay tolerance:** $\pm 5\%$ or better, or 2 ns whichever is greater.
- **Rise-time:** 2 ns typically.
- **Temperature coefficient:** 100 PPM/ $^{\circ}$ C.
- **Temperature range:** -0° C to 70° C standard. (-55° C to $+125^{\circ}$ C on request)*
- **Supply voltage:** 5 Vdc $\pm 5\%$.
- **Power dissipation:** 780 mw max.
- **DC Parameters:** See Fast-TTL Schottky Logic Table on Page 6.

*Add "M" after Part No. Example DDU-5J-1010M

** .320 Max. for "M" Units

| Part No. | Total Delay (ns) | Delay Per Tap (ns) |
|---------------|------------------|--------------------|
| DDU-5J-10050 | 50 | 5 |
| DDU-5J-10100 | 100 | 10 |
| DDU-5J-10150 | 150 | 15 |
| DDU-5J-10200 | 200 | 20 |
| DDU-5J-10250 | 250 | 25 |
| DDU-5J-10300 | 300 | 30 |
| DDU-5J-10400 | 400 | 40 |
| DDU-5J-10500 | 500 | 50 |
| DDU-5J-101000 | 1000 | 100 |
| DDU-5J-101500 | 1500 | 150 |
| DDU-5J-102000 | 2000 | 200 |

